



PTO/SB/21 (09-04)

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(to be used for all correspondence after initial filing)

Application Number	10/719,898
Filing Date	11/21/2003
First Named Inventor	WIECK, Christopher P.
Art Unit	2685
Examiner Name	Not Yet Assigned
Attorney Docket Number	81176/7114

Total Number of Pages in This Submission 5*

ENCLOSURES (CHECK ALL THAT APPLY)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to Technology Center (TC)
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<input checked="" type="checkbox"/> Information Disclosure Statement (3 pgs.)	<input type="checkbox"/> CD, Number of CD(s)	(4) References
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> Landscape Table on CD	Return Receipt Postcard (1)
<input type="checkbox"/> Response to Missing Parts/Incomplete Application	Remarks: *Exclusive of patent applications and publications.	
<input type="checkbox"/> Response to Missing Parts Under 37 CFR 1.52 or 1.53	Customer Number: 22242	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm name	Fitch, Even, Tabin & Flannery		
Signature			
Printed Name	Steven M. Freeland		
Date	June 23, 2005	Reg No.	42,555

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below.			
Typed or printed name	Susan R. Telford		
Signature		Date	June 23, 2005

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450 Alexandria, VA 22313-1450. DON NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): WIECK, Christopher P.

Appl. No.: 10/719,898

Filed: November 21, 2003

Title: DIRECT CONVERSION OF LOW
POWER HIGH LINEARITY RECEIVER

Group Art Unit: 2685

Examiner: Not Yet Assigned

Attorney Docket No.: 81176/7114

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06/23/2005

Date

Steven M. Freeland

Reg. No. 42,555

Attorney for Applicants

INFORMATION DISCLOSURE STATEMENT

MAIL STOP AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of international application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
- ☐ Statement under 37 CFR 1.97(e), or
 - ☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97(b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97(d) together with a:
- ☐ Statement under 37 CFR 1.97(e), and
 - ☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the issue fee)

The items AA-AB listed on PTO/SB/08B are in the English language, therefore, no further explanation is required.

Item AC is not in the English language and was cited by the U.S. Patent Office during the prosecution of U.S. Patent Publication No. 2004/0105511, cited in an Electronic IDS filed in the subject application on June 22, 2005. Applicant further provides herewith a copy of the translation of the Abstract of item AC and the following concise explanation of the relevance as is currently understood in compliance with 37 CFR 1.98(a)(3)(i):

PROBLEM TO BE SOLVED: To automatically control the power consumption of a circuit that extracts and processes only a desired signal from the sum signal resulting from two or more signals with different levels to which frequency division processing is applied.

SOLUTION: The receiver adopting a double superheterodyne system is used for a major building block. A detection circuit 410 detects the output of a 2nd IF amplifier 80 at the pre-stage of a 2nd IF filter 90 and automatically controls the bias currents of a 1st IF amplifier 60, a 2nd mixer 70 and the 2nd IF amplifier 80 depending on the magnitude of disturbance. The detector 410 increases the bias current when the level of a disturbing signal in existence in an adjacent channel and a channel next to the adjacent channel to the channel of a desired signal increases and decreases the bias current when the level of the disturbing signal decreases to control the power consumption.

Item AD is not in the English language and was cited by the U.S. Patent Office during the prosecution of U.S. Patent No. 6,625,238, cited in an Electronic IDS filed in the subject application on June 22, 2005. Applicant further provides herewith a copy of the translation of the Abstract of item AD and the following concise explanation of the relevance as is currently understood in compliance with 37 CFR 1.98(a)(3)(i):

PROBLEM TO BE SOLVED: To reduce the power consumption of the device and to suppress heat generation to improve the reliability.

SOLUTION: Bias currents I1, I2, and I3 are applied to amplification parts 3, 4, and 5, and an input signal S1 received through an antenna 1 and a band filter 2 is amplified. A reception electric field detection part 6 detects the level of the input signal S1 to output data on showing the reception electric field level. Optimum values D2 and D3 of bias currents I2 and I3 of amplification parts 4 and 5 which can suppress the intermodulation distortion within an allowable value to realize a low power consumption are preliminarily stored in a bias storage part 7 correspondingly to the

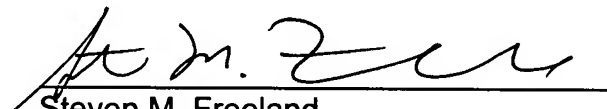
reception electric field level. A bias control part 8 receives data D1 showing the reception electric field level to read out pertinent optimum bias current values D2 and D2 from the bias storage part 7 and sends them to bias generation parts 9 and 10 respectively. Bias generation parts 9 and 10 generate bias currents 12 and 13 based on optimum current values D2 and D3 and supply them to amplification parts 4 and 5 respectively.

In accordance with the duty of disclosure imposed by 37 CFR § 1.56 to inform the Patent Office of all patents, publications, applications and other information known by Applicant or Applicant's representative that may be material to the examination of the subject application. Applicant's representative hereby provides this Information Disclosure Statement that is prepared in accordance with 37 CFR §§ 1.97-1.98 and includes Form PTO-SB/08A with four (4) references listed as items AA-AD and English language translations of the Abstracts for items AC-AD.

It is requested that the information disclosed herein be made of record in this application.

Date: June 23, 2005

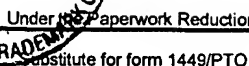
Respectfully submitted,


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(Use as many sheets as necessary)

Sheet	1	of	1
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First Named Inventor	WIECK, Christopher P.
Art Unit	2685
Examiner Name	Not Yet Assigned
Attorney Docket Number	81176/7114

[illegible]

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ *Number ⁴ *Kind Code ⁵ (if known)				
	AA	WO 03/063338 (PCT/US03/01060)	07-31-2003	Wieck		
	AB	WO 01/74000 (PCT/US01/10444)	10-04-2001	Wieck		
	AC	JP 200115665A	06-2001	Igarashi et al.		
	AD	JP 409107299A	04-1997	Mogi		

**Examiner
Signature**

Date
Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 (1-800-786-9199) and select option 2.